Computer Science 210

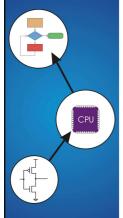
### **Computer Systems 1**

**Lecture Notes** 

Lecture 3

#### Introduction

Credits: Slides adapted from Gregory T. Byrd, North Carolina State University



# **Chapter 2**Bits, Data Types, and Operations

# How do we represent data in a computer?

At the lowest level, a computer is an electronic machine.

works by controlling the flow of electrons

Easy to recognize two conditions:

- 1. presence of a voltage we'll call this state "1"
- 2. absence of a voltage we'll call this state "o"

Could base state on value of voltage,

but control and detection circuits much more complex.

 compare turning on a light switch to measuring or regulating voltage

3

#### Computer is a Binary Digital System.

Digital system:

Binary (base two) system:

• finite number of symbols

• has two states: 0 and 1

Illegal

Analog Values → 0

Digital Values → "0" 0.5

"1" **=** 2.9 Volts 2.4

Basic unit of information is the binary digit, or bit.

Values with more than two states require multiple bits.

- A collection of two bits has four possible states:
- 00, 01, 10, 11
- A collection of three bits has eight possible states: 000, 001, 010, 011, 100, 101, 110, 111
- A collection of n bits has  $2^n$  possible states.

What kinds of data do we need to represent?

- Numbers signed, unsigned, integers, floating point, complex, rational, irrational, ...
- Text characters, strings, ...
- Images pixels, colors, shapes, ...
- Sound
- Logical true, false
- Instructions

Data type:

• representation and operations within the computer

We'll start with numbers...

#### **Unsigned Integers**

Non-positional notation

- could represent a number ("5") with a string of ones ("11111")
- problems?

Weighted positional notation

- like decimal numbers: "329"
- "3" is worth 300, because of its position, while "9" is only worth 9



most least significant 101 significant 22 21 20

3x100 + 2x10 + 9x1 = 329

1x4 + 0x2 + 1x1 = 5

Unsi	gned Integers (	cont )
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An *n*-bit unsigned integer represents any of  $2^n$  (integer) values: from 0 to  $2^{n-1}$ 

<b>2</b> <sup>2</sup>	21	<b>2</b> <sup>0</sup>	Value
0	0	0	0
0	0	1	1
0	1	0	2
0	1	1	3
1	0	0	4
1	0	1	5
1	1	0	6
1	1	1	7

How to convert decimal to binary video  $\underline{http://youtu.be/qWxiXU02ZQM}$ 

# **Unsigned Binary Arithmetic**

Base-2 addition − just like base-10!

• add from right to left, propagating carry

Subtraction, multiplication, division,...

"There are 10 kinds of people in the world: those who understand binary, and those who don't".

"There are  $10_2$  kinds of people in the world: those who understand binary, and those who don't".

"There are  $10_{10}$  kinds of people in the world: those who understand binary, and those who don't".

"There are  $10_{\rm two}$  kinds of people in the world: those who understand binary, and those who don't".

-- http://en.wikipedia.org/wiki/Mathematical\_jok

#### **Signed Integers**

With n bits, we can distinguish  $2^n$  unique values

- assign about half to positive integers (1 through  $2^{n-1}$ ) and about half to negative ( $-2^{n-1}$  through -1)
- that leaves two values: one for o, and one extra

#### Positive integers

• just like unsigned, but zero in most significant (MS) bit

#### Negative integers

- Sign-Magnitude (or Signed-Magnitude) set MS bit to show negative, other bits are the same as unsigned 10101 = -5
- One's complement flip every bit to represent negative 11010 = -5
- $\blacksquare$  In either case, MS bit indicates sign: o=positive, 1=negative
- Video: http://youtu.bo

#### **Two's Complement**

Problems with sign-magnitude and 1's complement

- ${\color{red}\bullet}$  two representations of zero (+o and -o)
- arithmetic circuits are complex
  - How to add two sign-magnitude numbers?
    - e.g., try 2 + (-3)
  - How to add two one's complement numbers? -e.g., try 4 + (-3)

Two's complement representation developed to make circuits easy for arithmetic.

• for each positive number (X), assign value to its negative (-X), such that X + (-X) = 0 with "normal" addition, ignoring carry out

#### **Two's Complement**

To get a negative number first "flip the bits" of its positive binary representation

Then add 1

#### **Two's Complement Signed Integers**

MS bit is sign bit

Range of an n-bit number:  $-2^{n-1}$  through  $2^{n-1} - 1$ 

■ The most negative number  $(-2^{n-1})$  has no positive counterpart.

0 0 0 0 0 1 0 0 -8	
	_
0 0 0 1 1 1 0 0 1 -7	
0 0 1 0 2 1 0 1 0 -6	
0 0 1 1 3 1 0 1 1 -5	
0 1 0 0 4 1 1 0 0 -4	
0 1 0 1 5 1 1 0 1 -3	
0 1 1 0 6 1 1 1 0 -2	
0 1 1 1 7 1 1 1 1 -1	

# "Biased" Representation of Signed Integers

All integers (positive & negative) are represented as an unsigned integer supplemented with a "bias" to be subtracted out.

Range of an n-bit number: (o - bias) through ( $2^n$ -1 - bias).

Bias 7:

7:	<b>2</b> <sup>3</sup>	2 <sup>2</sup>	<b>2</b> <sup>1</sup>	<b>2</b> <sup>0</sup>	Bias-7	<b>2</b> <sup>3</sup>	<b>2</b> <sup>2</sup>	<b>2</b> <sup>1</sup>	20	Bias-7
•	0	0	0	0	-7	1	0	0	0	1
	0	0	0	1	-6	1	0	0	1	2
	0	0	1	0	-5	1	0	1	0	3
	0	0	1	1	-4	1	0	1	1	4
	0	1	0	0	-3	1	1	0	0	5
	0	1	0	1	-2	1	1	0	1	6
	0	1	1	0	-1	1	1	1	0	7
	0	1	1	1	0	1	1	1	1	8

# Converting Binary (2's C) to Decimal

1. If leading bit is zero (a positive number) just convert as normal

Х	=	01101000
	=	2 <sup>6</sup> +2 <sup>5</sup> +2 <sup>3</sup>
	=	64+32+8
x	=	104



Assuming 8-bit 2's complement numbers.

-15

### Converting Binary (2's C) to Decimal

Same as before **EXCEPT** the MS bit is negative

$$X = 11100110$$

$$= -2^{7} + 2^{6} + 2^{5} + 2^{2} + 2^{1}$$

$$= -128 + 64 + 32 + 4 + 2$$

$$X = -26$$

Assuming 8-bit 2's complement numbers.

### Converting Decimal to Binary (2's C)

First Method: Division

- 1. Find magnitude of decimal number. (Always positive.)
- 2. Divide by two  ${\sf -}$  remainder is least significant bit.
- 3. Keep dividing by two until answer is zero, writing remainders from right to left.
- Append a zero as the MS bit; if original number was negative, take two's complement.

# Converting Decimal to Binary (2's C)

Second Method: Subtract Powers of Two

- 1. Find magnitude of decimal number.
- 2. Subtract largest power of two less than or equal to number.
- 3. Put a one in the corresponding bit position.
- 4. Keep subtracting until result is zero.
- 5. Append a zero as MS bit; if original was negative, take two's complement.

X = 104 <sub>ten</sub>	104 - 64 = 40 - 32 = 8 - 8 =	8	bit 6 bit 5 bit 3
$X = 01101000_{two}$			

0 1 1 2 2 4 3 8 4 16 5 32 6 64