| <section-header><section-header><section-header><section-header><section-header><section-header><section-header><section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header> | <section-header><section-header><section-header><section-header><section-header><section-header><section-header><section-header><section-header><section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header> | <section-header><section-header><section-header><section-header><section-header><section-header><section-header><section-header><section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header></section-header> |
|---|--|--|
| Recommended Readings For today's lecture Chapter 11: function invocation. Randy Bryant, <i>Alpha Assembly Language Guide</i> (available under Resources at the website) Section 3 Chapter 12: assembling and disassembling For the mini-assignment Chapter 6: program structure Chapter 7: strings Chapter 8: running the simulator Chapter 10: writing and debugging in assembly language | Caller/Callee Register Allocation Temporary registers for callee \$t0-\$t11 Free for use, but not preserved Saved registers for caller \$s0-\$s5 Free to use, but responsible for saving/restoring value Every method is potentially both a caller and callee Leaves (methods that invoke no other methods) often don't need to use S registers—no spills Other nodes save registers they use exactly once: on invocation | Dealing with Arguments Used for communication between caller and callee No limit to number of allowed arguments Pass arguments in registers: Sa0-Sa5 Pass additional arguments through stack Argument registers Sa0-Sa5 are like temporaries Must be preserved if needed after a call If not needed, can be used as a temporary |
| <section-header> 2000000000000000000000000000000000000</section-header> | 25.49.67 C5270 5 Use of Stack for Subroutines: Callee Allocate space for new activation record Saved any saved registers (\$s0-\$s11) to stack Save \$ra to stack if any other procedure might be called Perform function (possibly invoking other functions) Perform saved registers (\$c0 \$c11 \$cn) | 2040-07 CERT OF CERTO |

- Caller executes bsr instruction
- Address of subsequent instruction stored in \$ra
- Jumps to beginning of callee
- On return

26-Apr-07

 Restore arguments (\$a0-\$a5) and tmps (\$t0-\$t5) if/when needed

CS210

- Restore saved registers (\$s0-\$s11, \$ra)
- Assign return value to \$v0
- Deallocate space for current activation record
- Return to calling procedure via \$ra

- the relative address (relative to the top of stack) is known: a small constant
- Addressing mode of base register + displacement is perfect
 - base: frame pointer (or stack pointer)
 - displacement (computed when the stack frame is laid out.

CS210

7

26-Apr-07

CS210

8

26-Apr-07

9



- Read program and parse
 - Identify declarations of instructions and static data

CS210

- identify pseudo-instructions
- Allocate space for instructions and data
- Recognize labels
 - · Define address if possible
 - Remember for future reference

16